

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>90</b>	<b>714/9.ccls.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 08:11</b>
<b>2</b>	<b>468</b>	<b>714/11.ccls.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:32</b>
<b>3</b>	<b>153</b>	<b>marisetty</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:32</b>
<b>4</b>	<b>0</b>	<b>"suresh marisetty"</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:33</b>
<b>5</b>	<b>0</b>	<b>suresh adj marisetty</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:33</b>
<b>6</b>	<b>4888</b>	<b>suresh marisetty</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:33</b>
<b>7</b>	<b>8</b>	<b>marisetty-suresh.in.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:33</b>
<b>8</b>	<b>144</b>	<b>714/12.ccls. not 714/11.ccls.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:35</b>
<b>9</b>	<b>261</b>	<b>714/13.ccls. not 714/11,12.ccls.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:40</b>
<b>10</b>	<b>686</b>	<b>714/48.ccls.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/07/12 09:47</b>

-	87	714/9.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 08:11
-	9	714/9.ccls. and (system adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:39
-	3234	direct adj memory adj access adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:40
-	8466	"dma" adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:40
-	1091	(direct adj memory adj access adj controller) and (error errors fault faults)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:40
-	3656	("dma" adj controller) and (error errors fault faults)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:40
-	1006	((direct adj memory adj access adj controller) and (error errors fault faults)) and (dual two multiple)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:41
-	680	("dma" adj controller) near6 (dual two multiple)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 13:41
-	135	(direct adj memory adj access adj controller) near6 (dual two multiple)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:02
-	5	((direct adj memory adj access adj controller) near6 (dual two multiple)) and 714/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:02

-	482	"pci" adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:41
-	16	("pci" adj controller) near6 (dual multiple two)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:03
-	18	peripheral adj component adj interconnect adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:26
-	272	("pci" adj controller) same ("pci" adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:42
-	367	("pci" adj controller) and ("pci" adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:45
-	151	((("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 14:45
-	430	714/11.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 09:02
-	31	714/11.ccls. and (system adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 16:02
-	139	714/12.ccls. not 714/11.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 09:35
-	237	714/13.ccls. not 714/11,12.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 09:40

-	630	714/48.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 09:47
-	68	address adj arbiter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 18:39
-	3894	(error) near6 interrupt	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/31 18:39
-	63	(error adj message) near6 interrupt	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 12:12